



#13/A
OK ON
5/16/01

Attorney Docket No. SEL 126

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Takeshi FUKUNAGA

Serial No.: 09/262,657

Filed: March 4, 1999

Examiner: Mark Prenty

Art Unit: 2822

For: SEMICONDUCTOR DEVICE AND
METHOD FOR MANUFACTURING
SAME

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on April 19, 2001

Signature *[Handwritten Signature]*

Date April 19, 2001

Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

Dear Sir:

Please cancel claims 16-29 without prejudice or disclaimer.

Claims 1-15 and 30-38 are to read as follows:

1. (Amended) A semiconductor device comprising:
- a substrate;
 - a first thin film transistor having a first active layer comprising $\text{Si}_{1-x}\text{Ge}_x$ where $0 < x < 1$ formed over said substrate; and
 - a second thin film transistor having a second active layer comprising silicon formed over said substrate wherein said second active layer is not intentionally doped with germanium.

A1
Con4
Rubin

RECEIVED
MAY 30 2001
TECHNOLOGY CENTER 2800